

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Harari et al.		
Title:	Flash EEPROM System with Programming Verification		
Application No.:	09/129,675	Filing Date:	August 5, 1998
Examiner:	A. Tran	Group Art Unit:	2824
Docket No.:	SNDK.006USS	Conf. No.:	4949

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Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

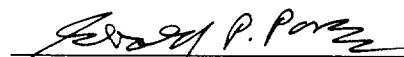
**SUBMISSION OF REPLACEMENT DRAWINGS**

Dear Sir:

Applicants submit six (6) sheets of replacement drawings and six (6) sheets of annotated drawings, consisting of Figures 7, 8, 13, 17A, 17B, 21B, 21C, and 22, in the above-named application. No new matter has been added. If there are any questions regarding these drawings, please call the undersigned.

**FILED VIA EFS**

Respectfully submitted,



Gerald P. Parsons  
Reg. No. 24,486

10/25/06  
Date

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